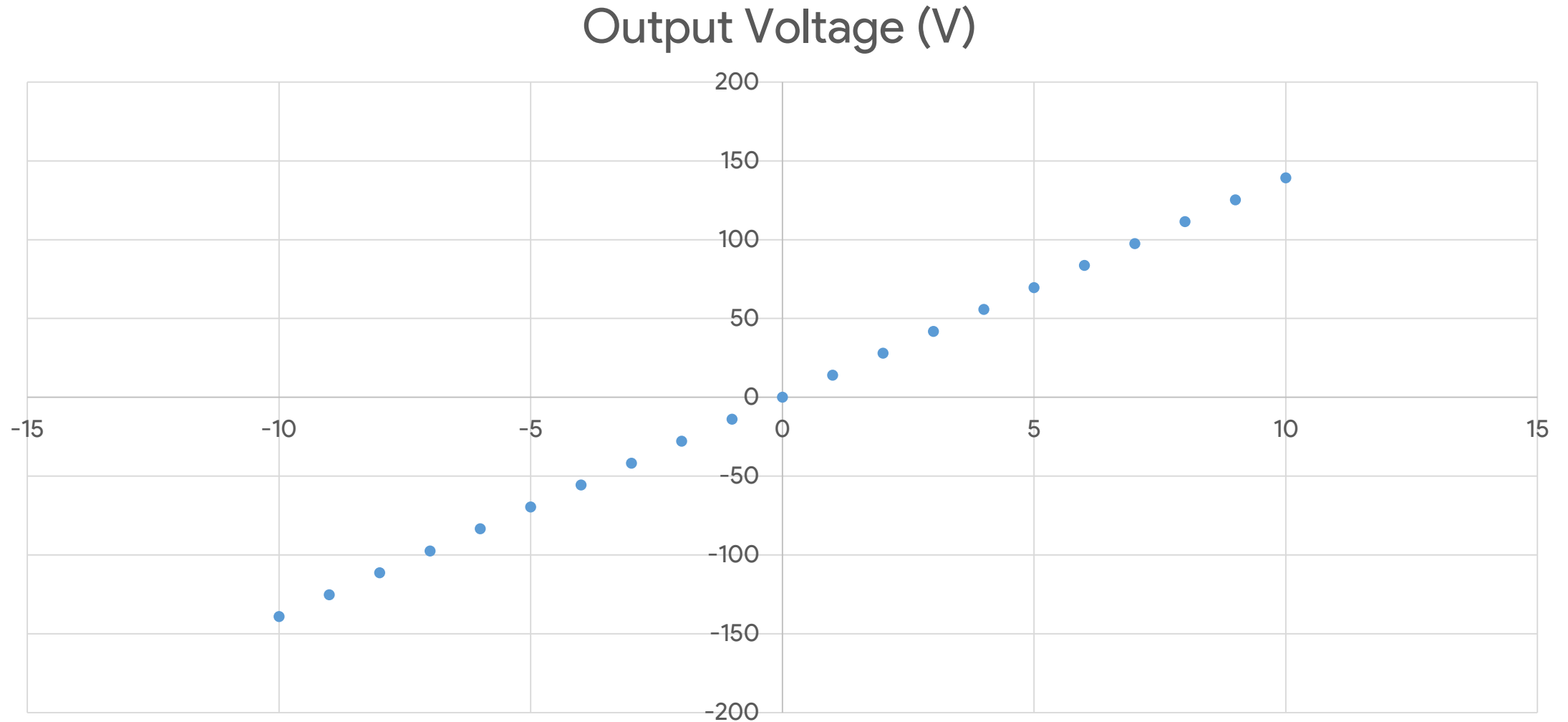


Trap

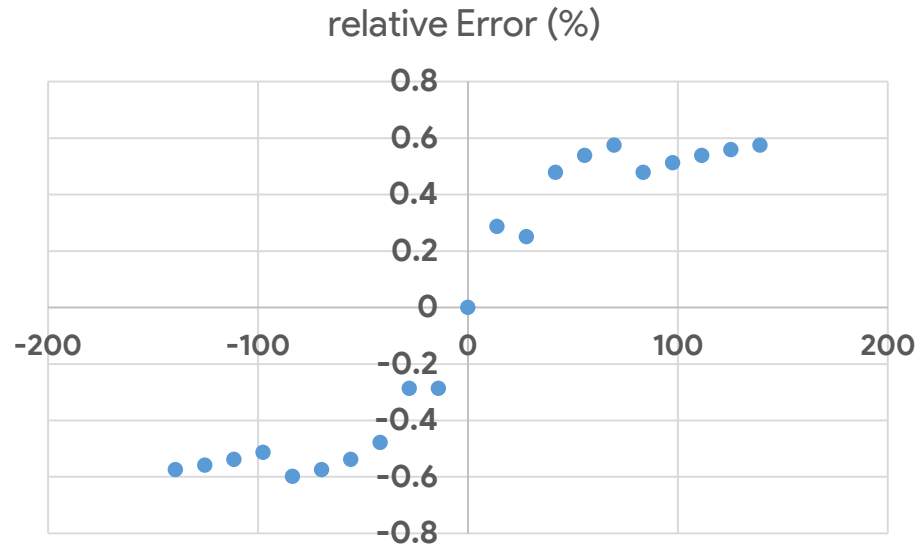
Dirk's Amp & Sequence test

박관형

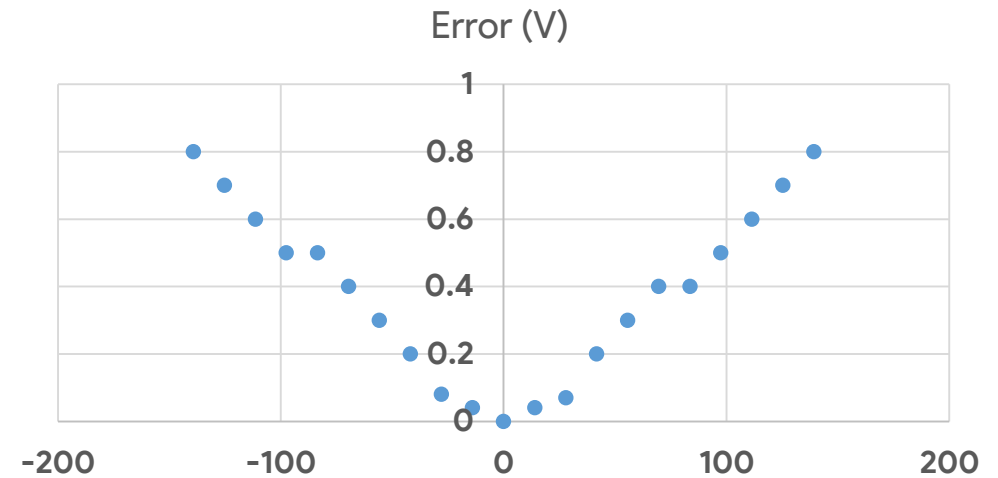
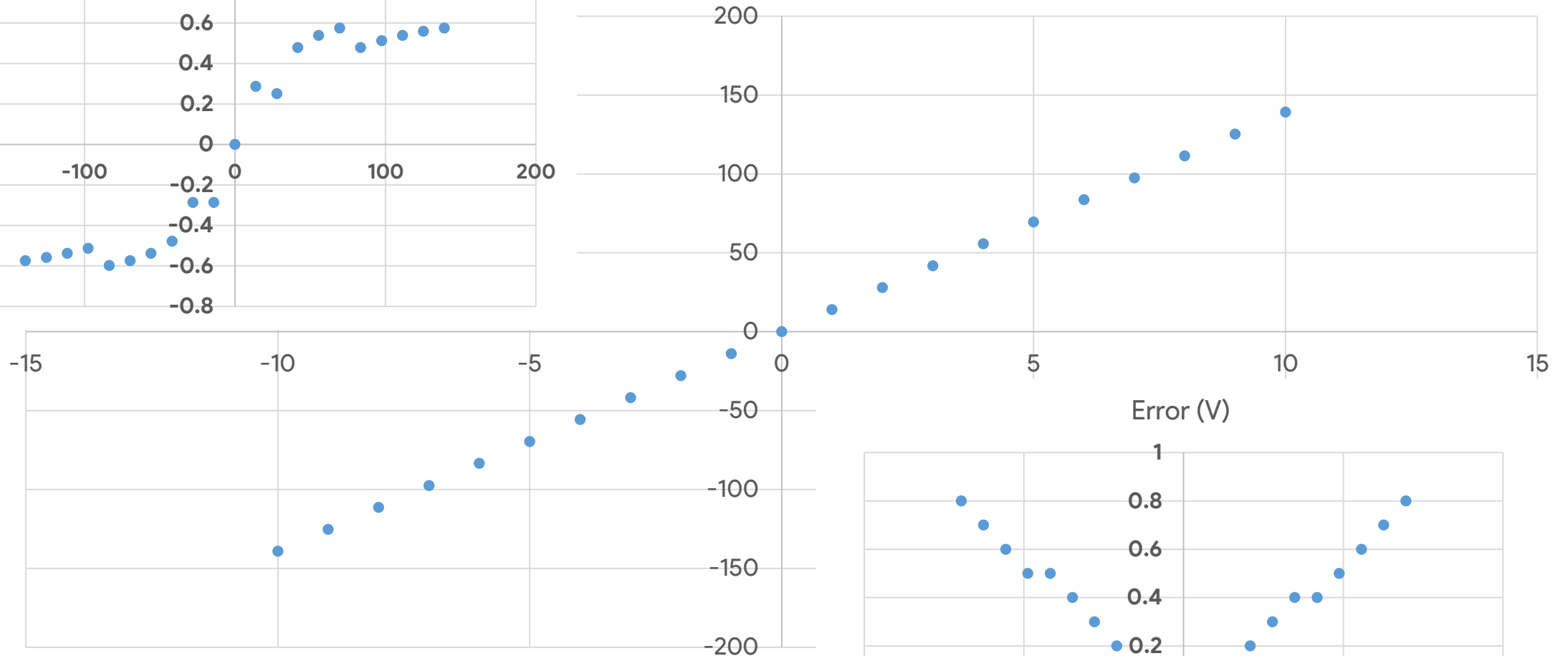
Amp test



Amp test



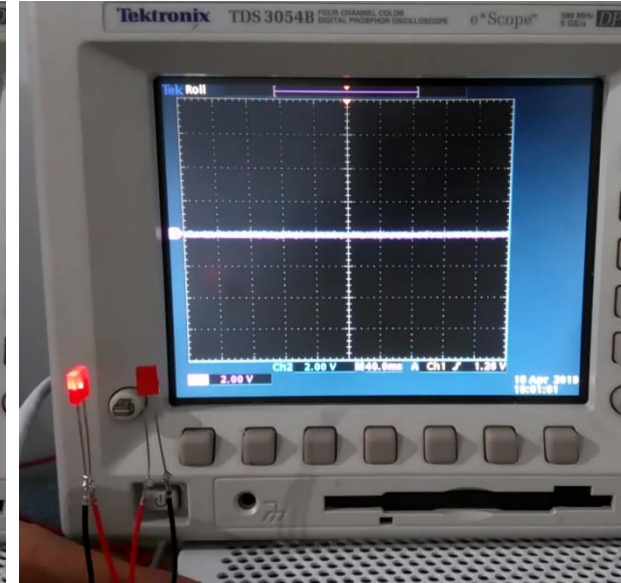
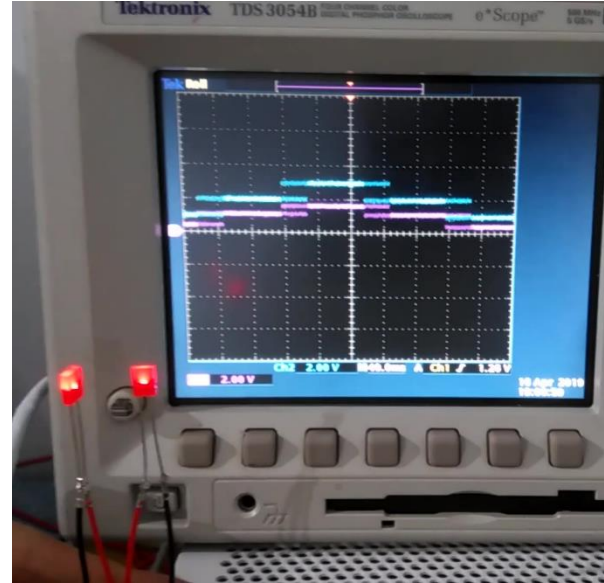
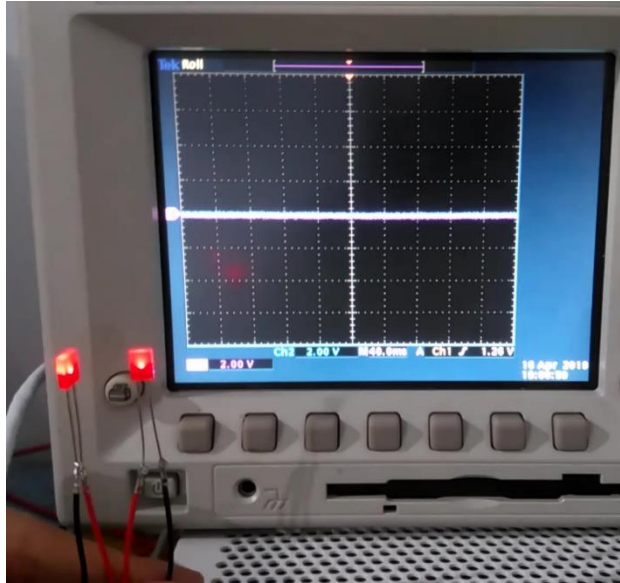
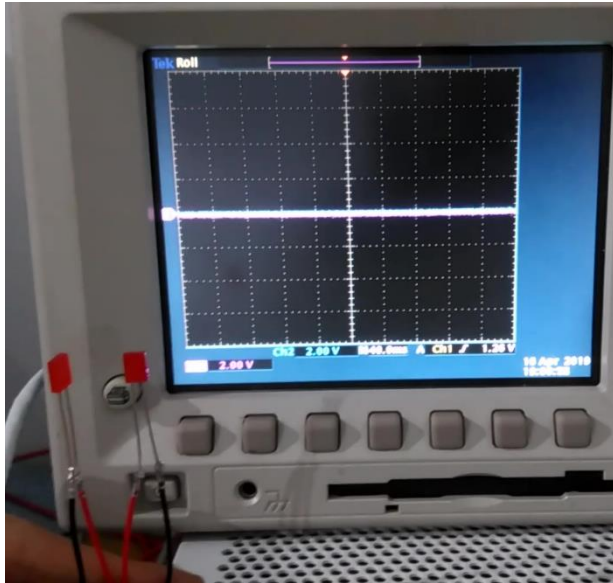
Output Voltage (V)



Full-Range Gauge



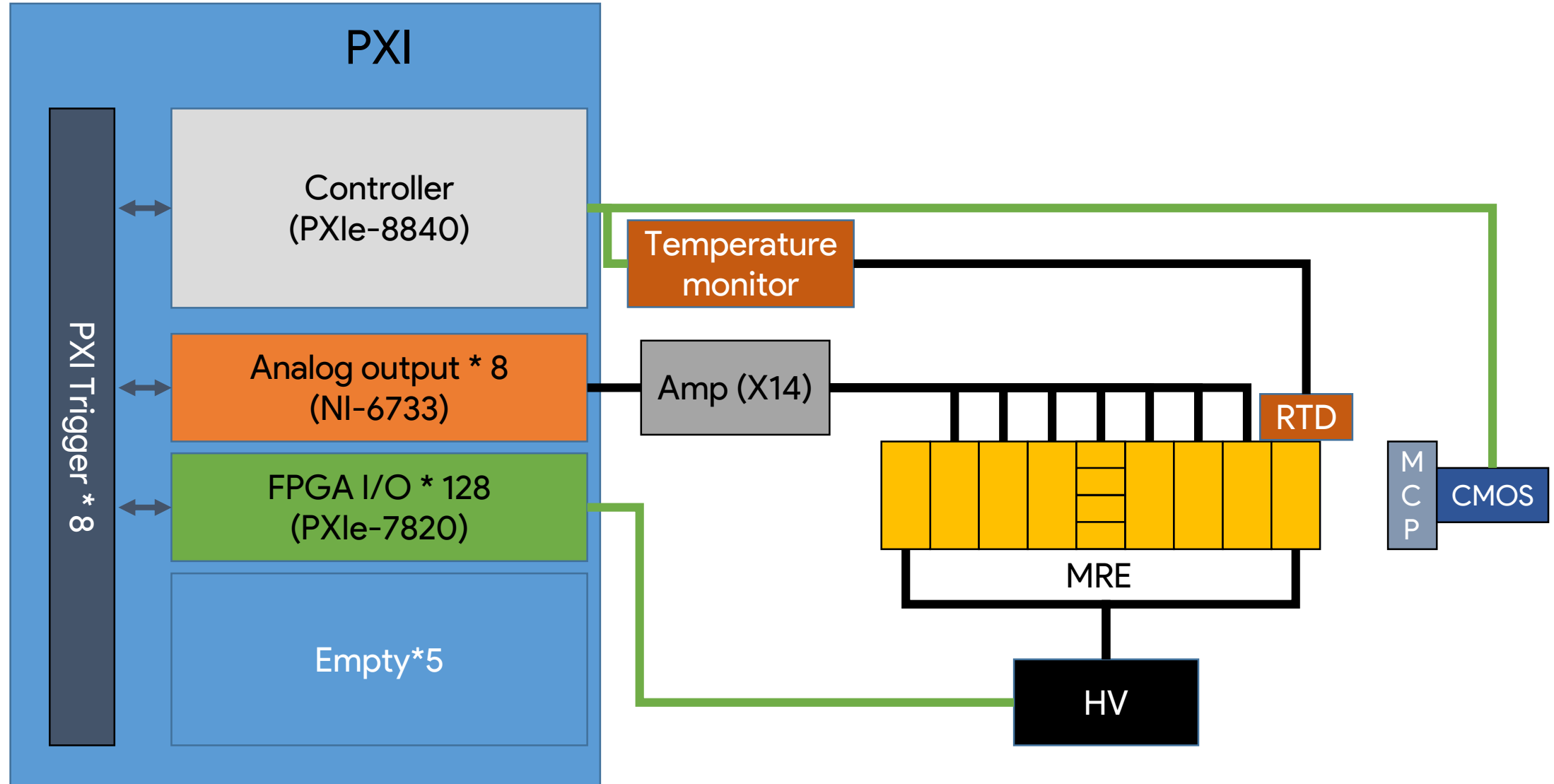
Sequence Test



- | | | |
|---------------------------|-----------|---------------|
| 1. LED L,R ON | (NI-7820) | ↩ PXI-trigger |
| 2. Voltage sequence start | (NI-6733) | |
| 3. LED R OFF | (NI-7820) | ↩ PXI-trigger |

Total operation time (6.002)= Sequence time (6s) + delay (2.154 ms)

PXI control part



NI-6733 Control Panel

Output Channel		MRE Voltage				rate(sequence/second)	RW Start time (s)
I% Dev1/ao0:3	0	1.00	2.00	3.00	3.00	1.00	1
Trigger source (start)	0	1.20	2.00	5.00	2.30		2
I% /Dev1/PXI_Trig0		0.00	3.10	1.00	0.00		3
sample mode		5.00	6.00	4.00	5.00		4
Finite Samples		2.00	3.00	1.00	5.00		5
		0.00	0.00	0.00	0.00		6

error out (MRE volt)		error out (RW trigger)	
status	code	code	
☒	d0	d0	
source			

Todo list

- Gate valve interlock with Gauge controller

- Sequence editor upgrade
 - Interpolation
 - Comment section
 - Optimizer

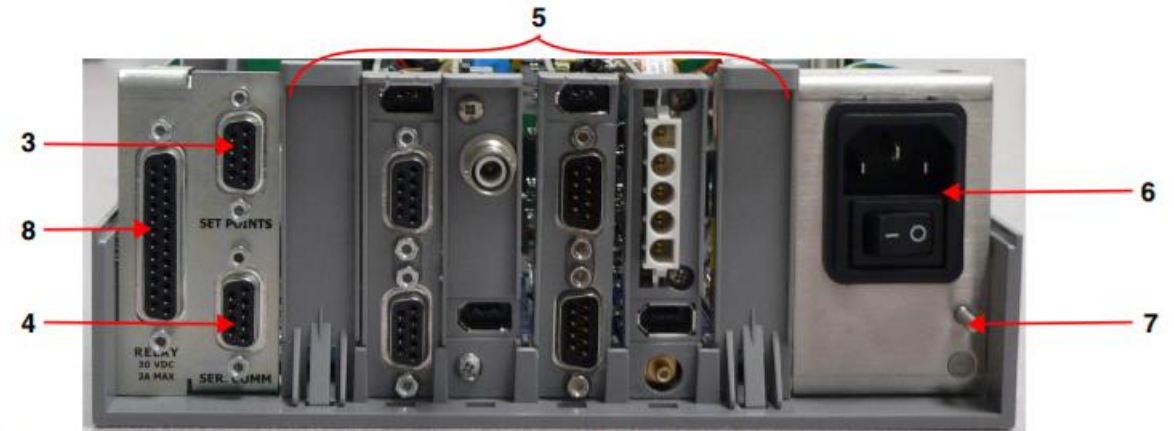
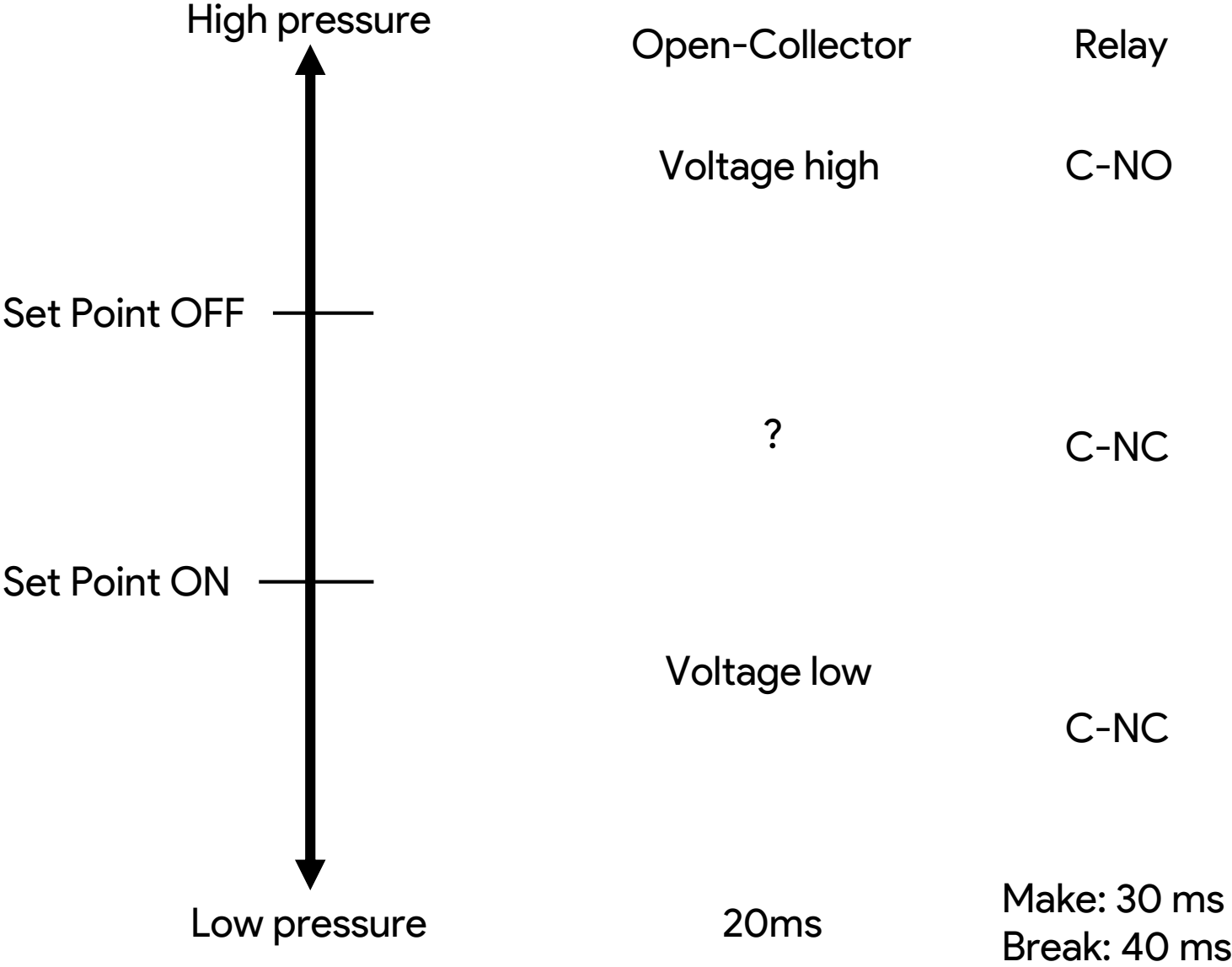


Figure 1-2 Backplate Connections (CNV, IMG, Analog and HFIG boards shown)

Table 1-1 Front/Rear Panel Components

Item	Description	Page
1	Display	See "Screen Flow" on page 23.
2	Keypad/Navigation Keys	See "General Navigation and Data Entry" on page 24.
3	Open-Collector Set Point Outputs	See "Set-Point Connector Pin Assignments" on page 61.

Set Point



Amp I/O (V)

Input Voltage (NI-6733)	Output Voltage (Amp)	Input Voltage (NI-6733)	Output Voltage (Amp)
10	139.2	-1	-13.96
9	125.3	-2	-27.92
8	111.4	-3	-41.8
7	97.5	-4	-55.7
6	83.6	-5	-69.6
5	69.6	-6	-83.5
4	55.7	-7	-97.5
3	41.8	-8	-111.4
2	27.93	-9	-125.3
1	13.96	-10	-139.2

Oscilloscope at KU

