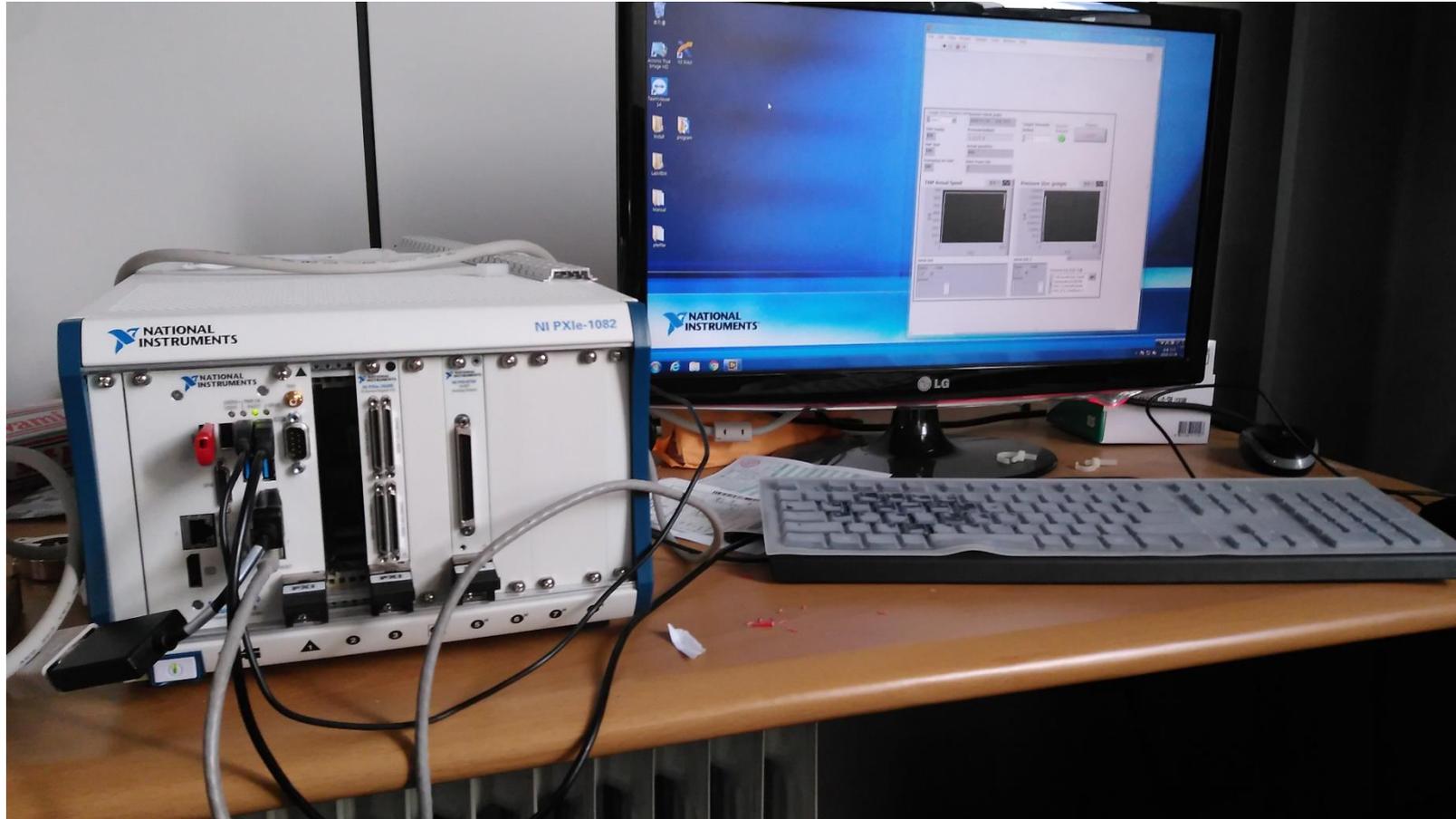
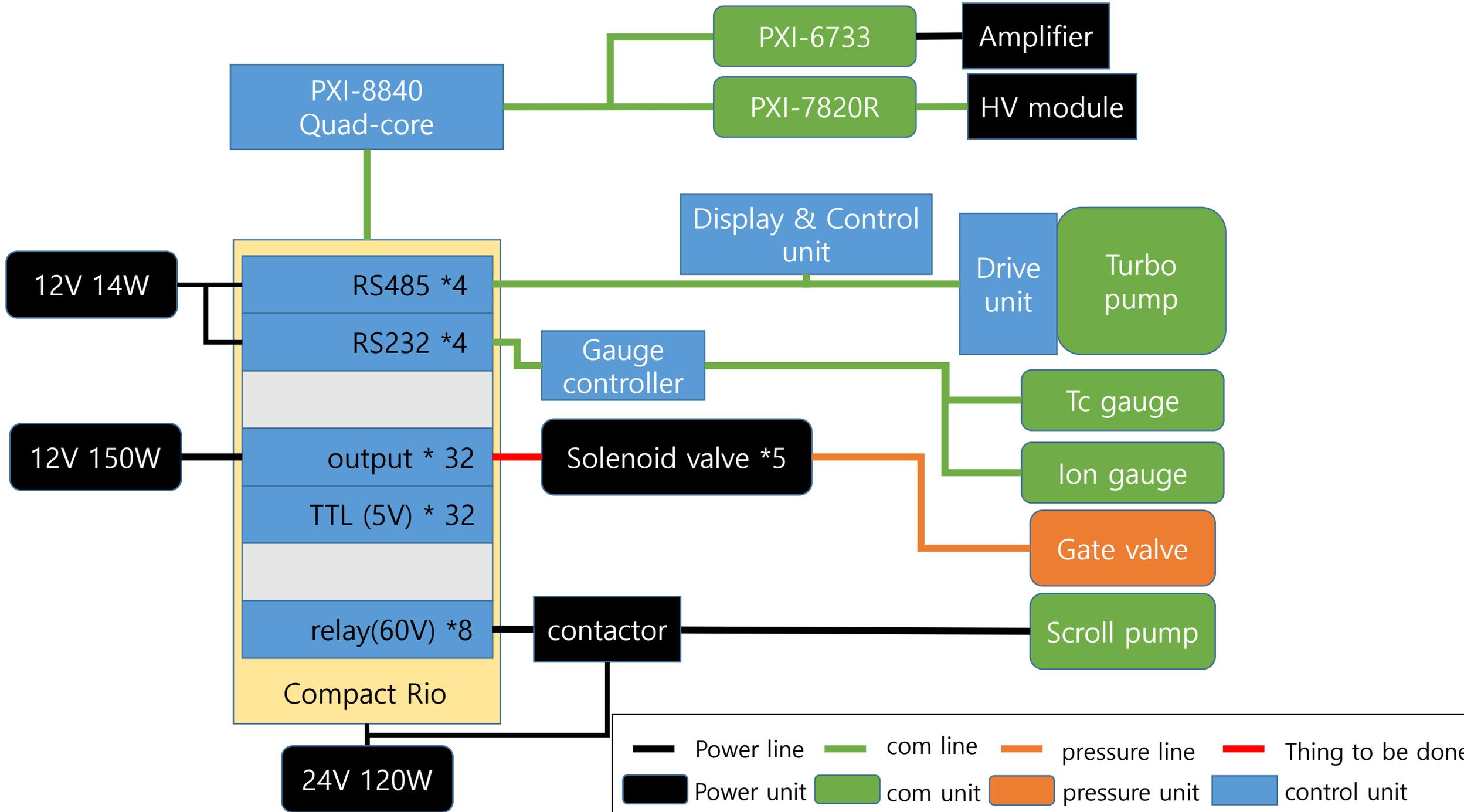


Trap

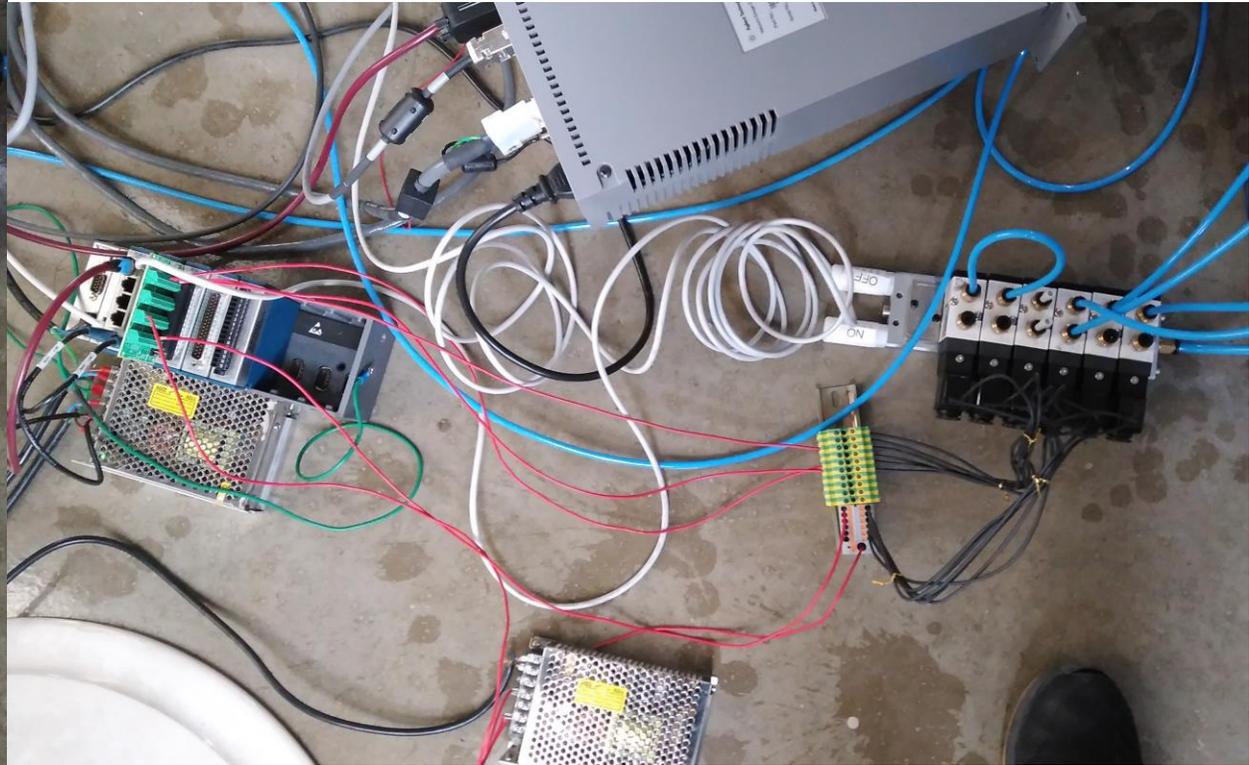
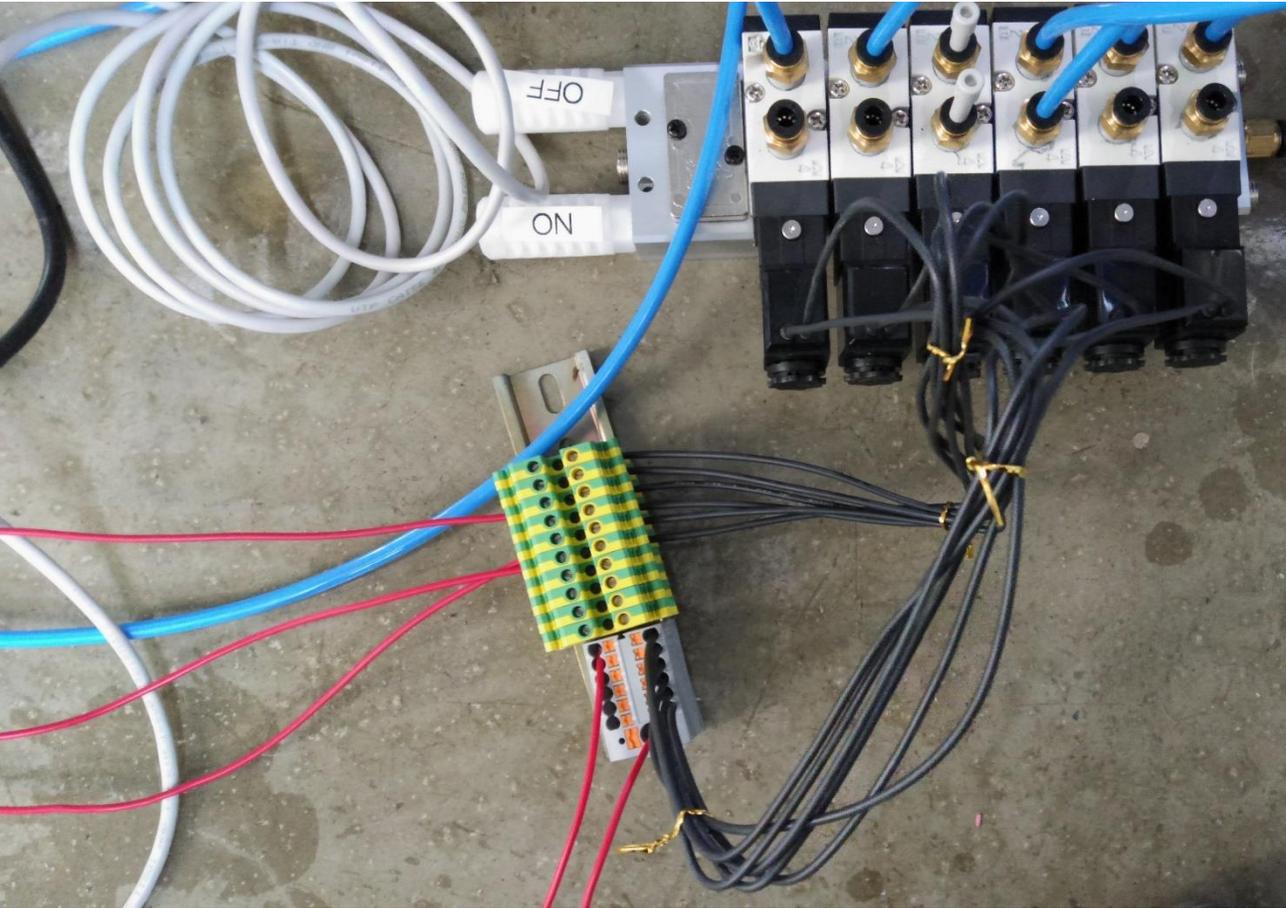
PXI-system and etc

Pxi system

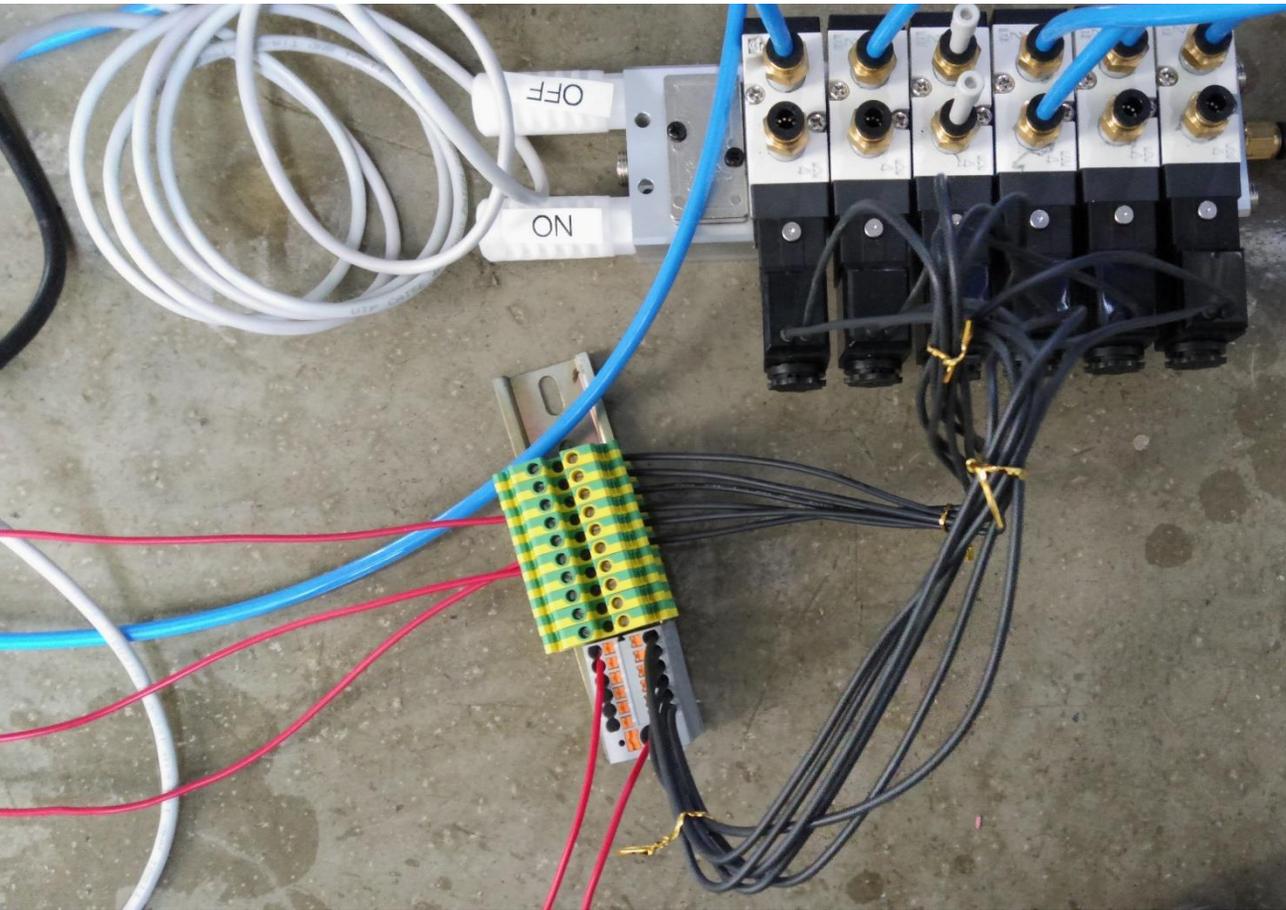




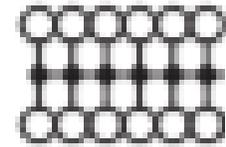
Solenoid valve control



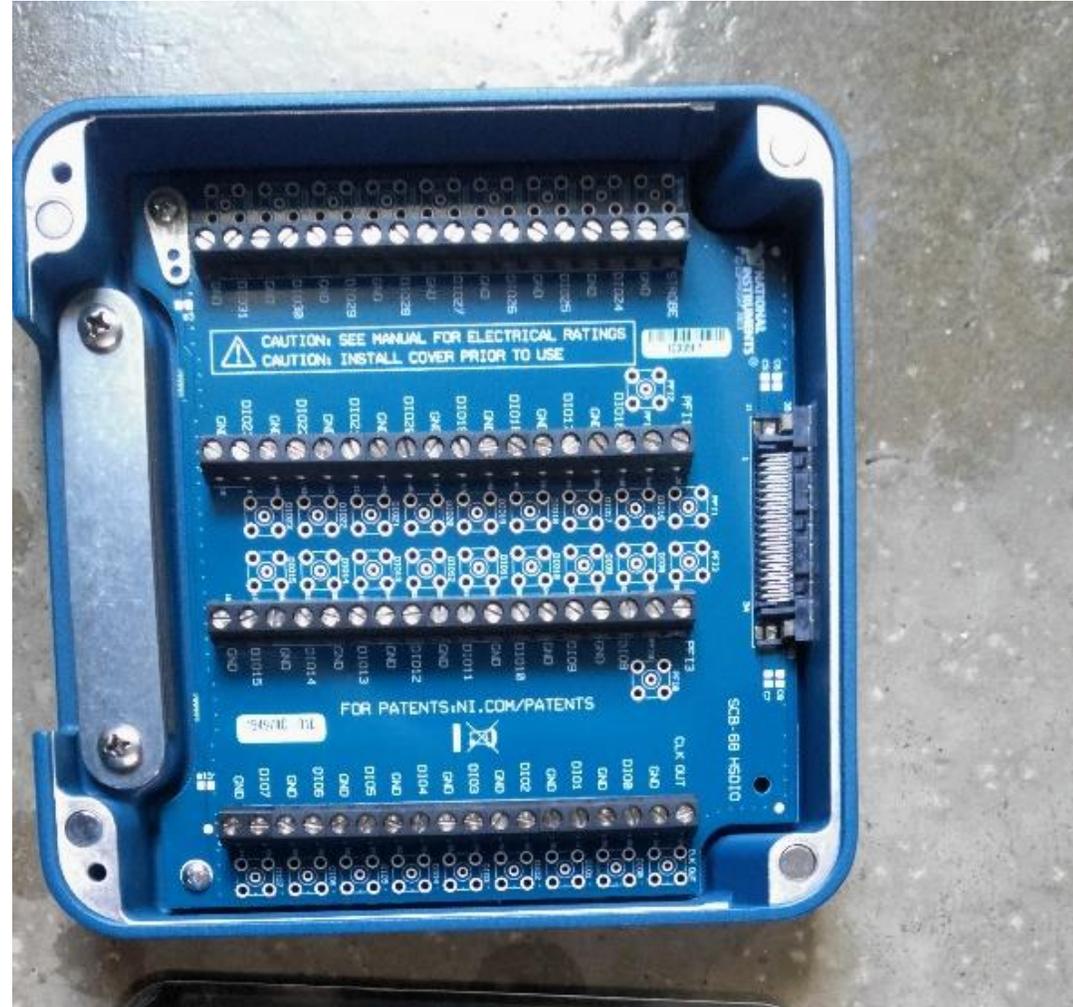
Solenoid valve control



Circuit diagram

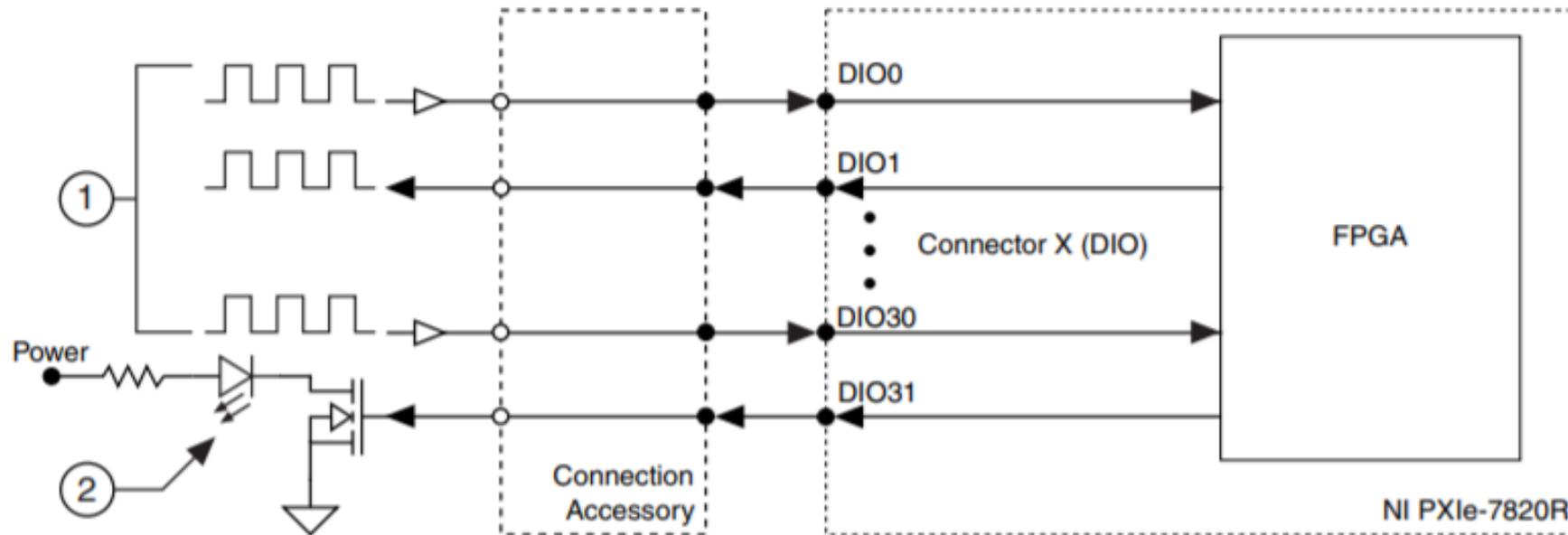


Pxi-7820R connection



Pxi-7820R connection

Figure 4. Connecting to the DIO Channels



1. High-speed signal frequencies up to 80 MHz with logic levels configured as 1.2 V, 1.5 V, 1.8 V, 2.5 V, or 3.3 V
2. LED

Todo

- PXI-6733
 - DaqMX

- PXI-7820R
 - FPGA