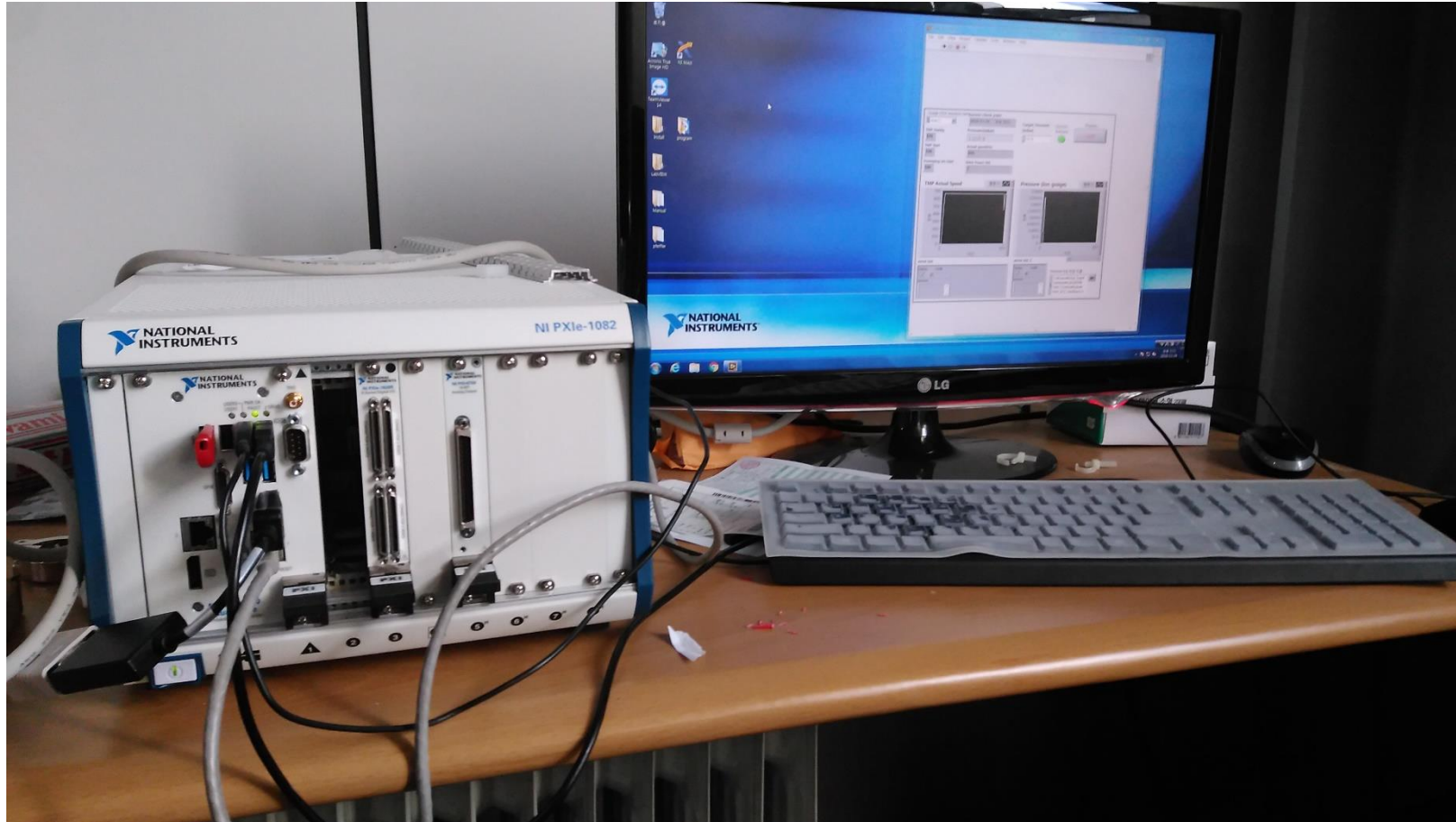
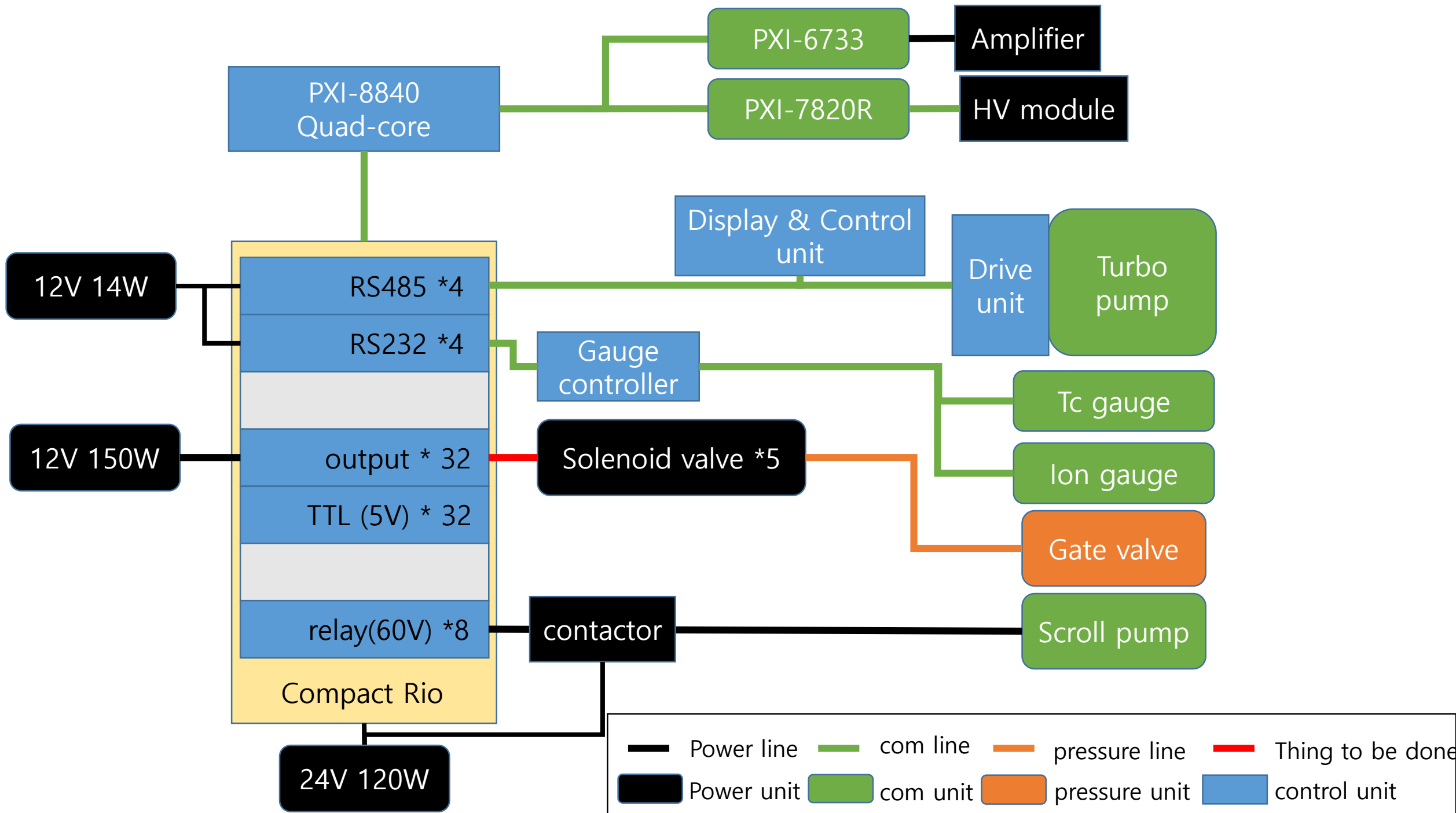


Trap

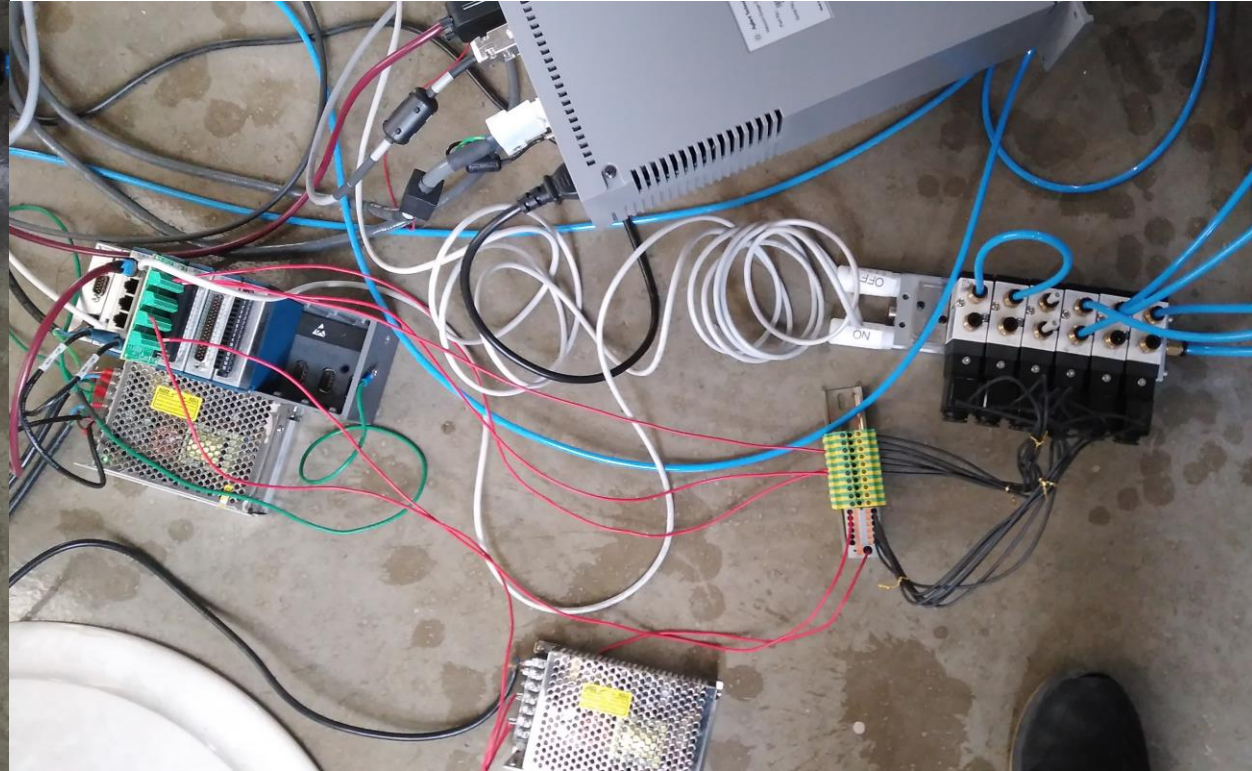
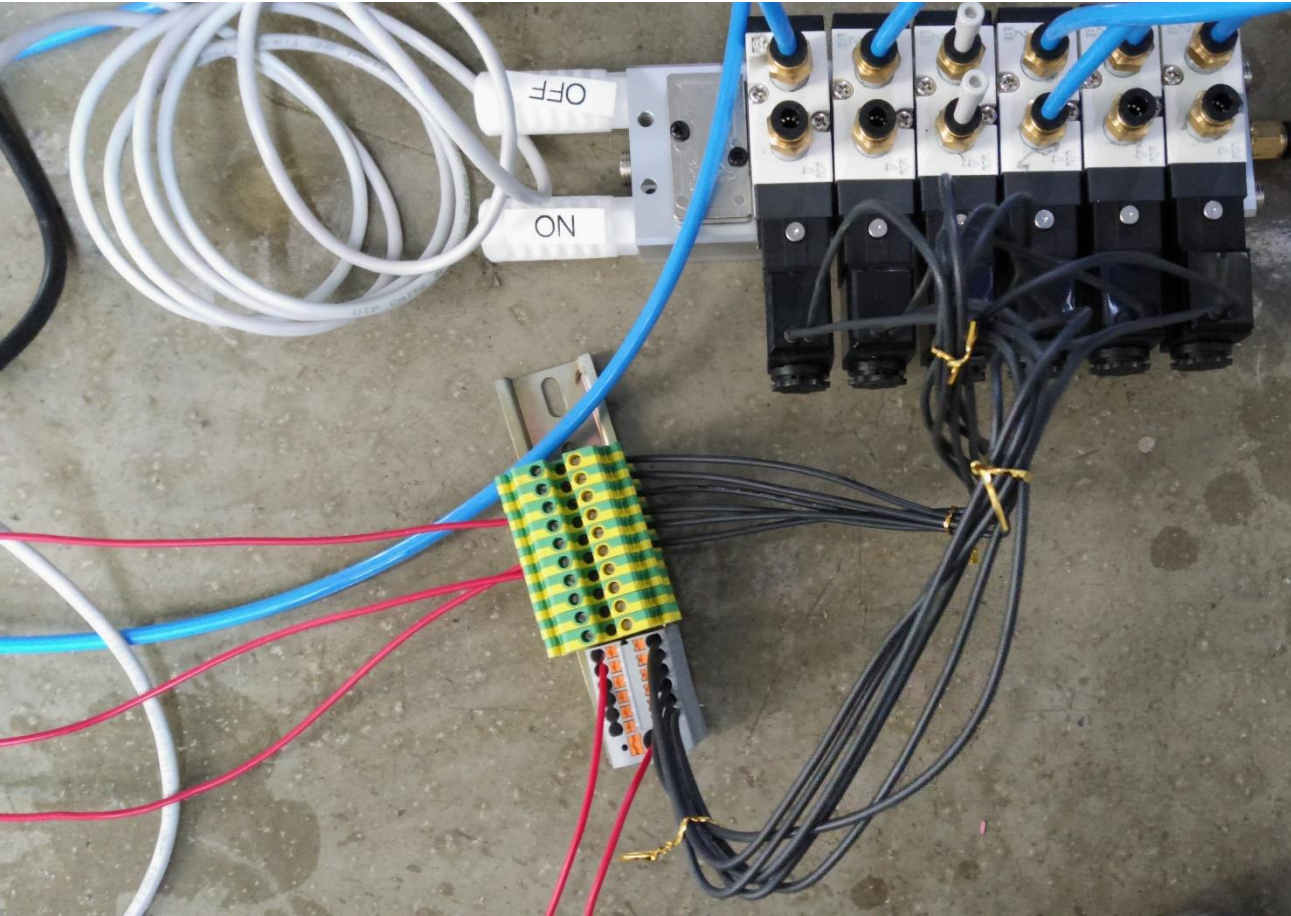
PXI-system and etc

Pxi system

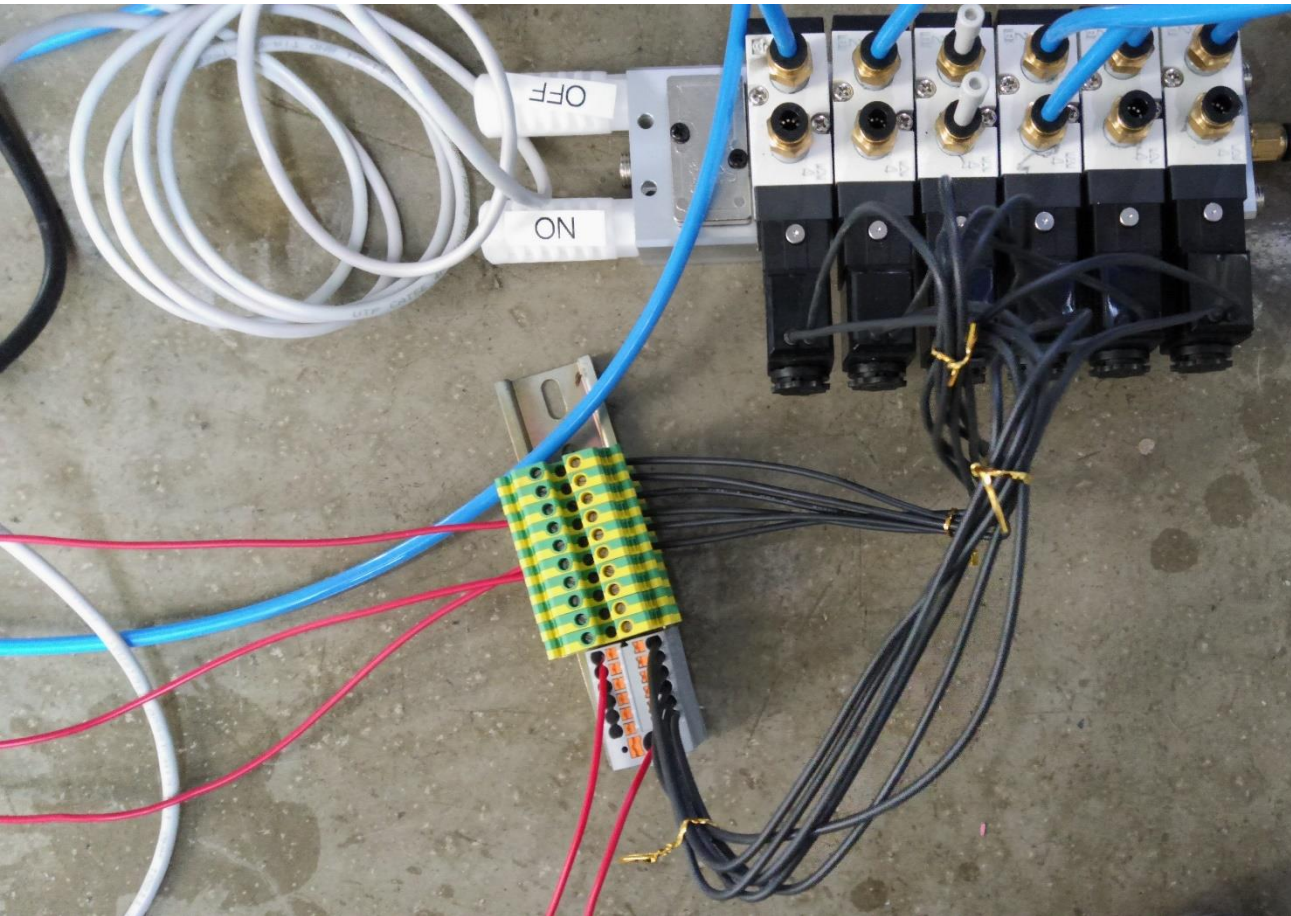




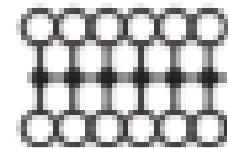
Solenoid valve control



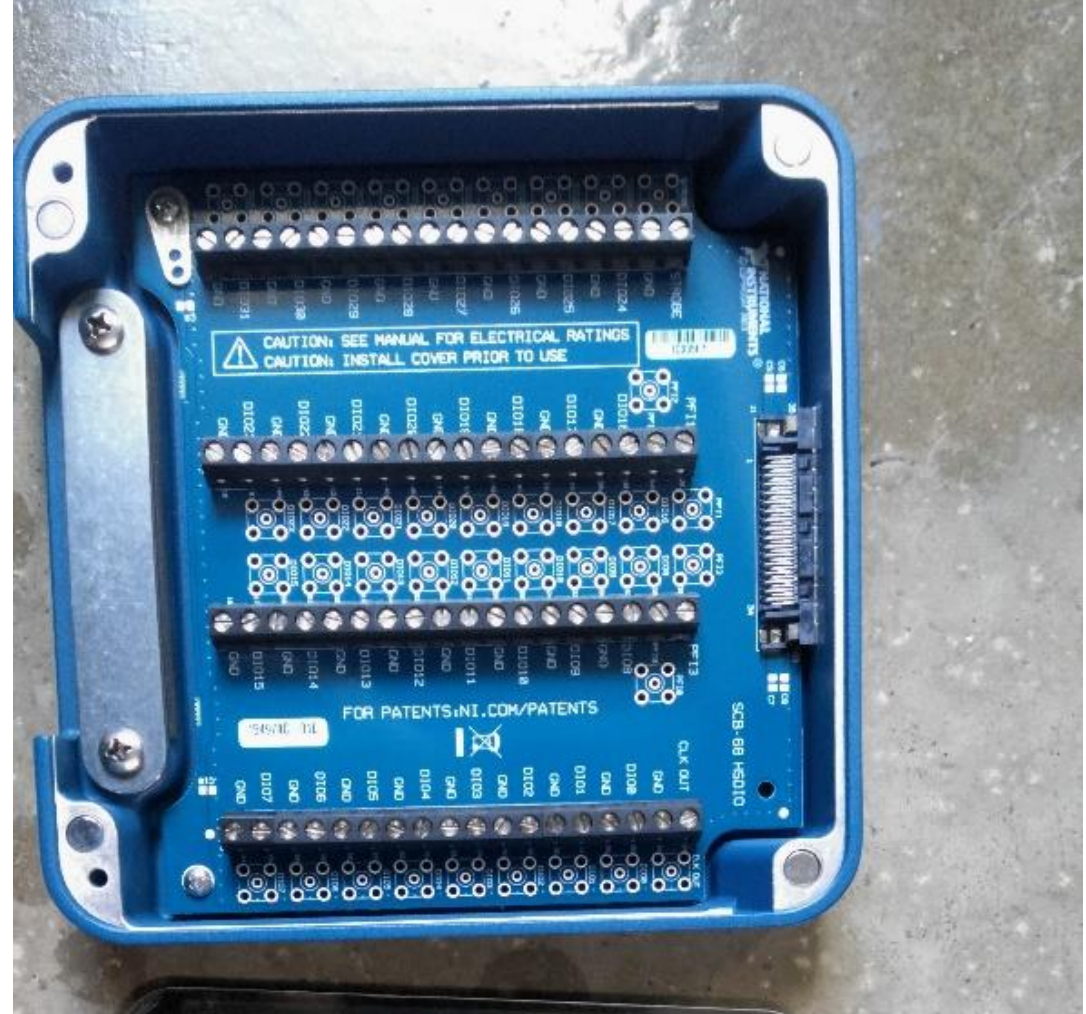
Solenoid valve control



Circuit diagram

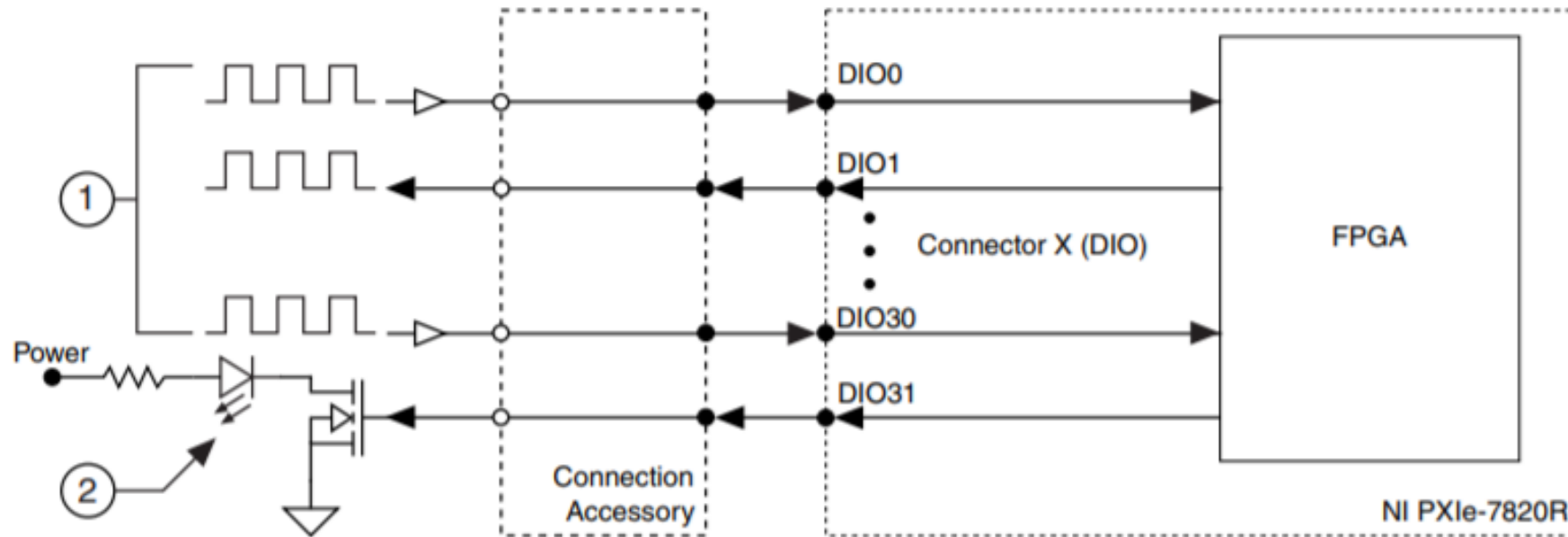


Pxi-7820R connection



Pxi-7820R connection

Figure 4. Connecting to the DIO Channels



Todo

- PXI-6733
 - DaqMX
- PXI-7820R
 - FPGA