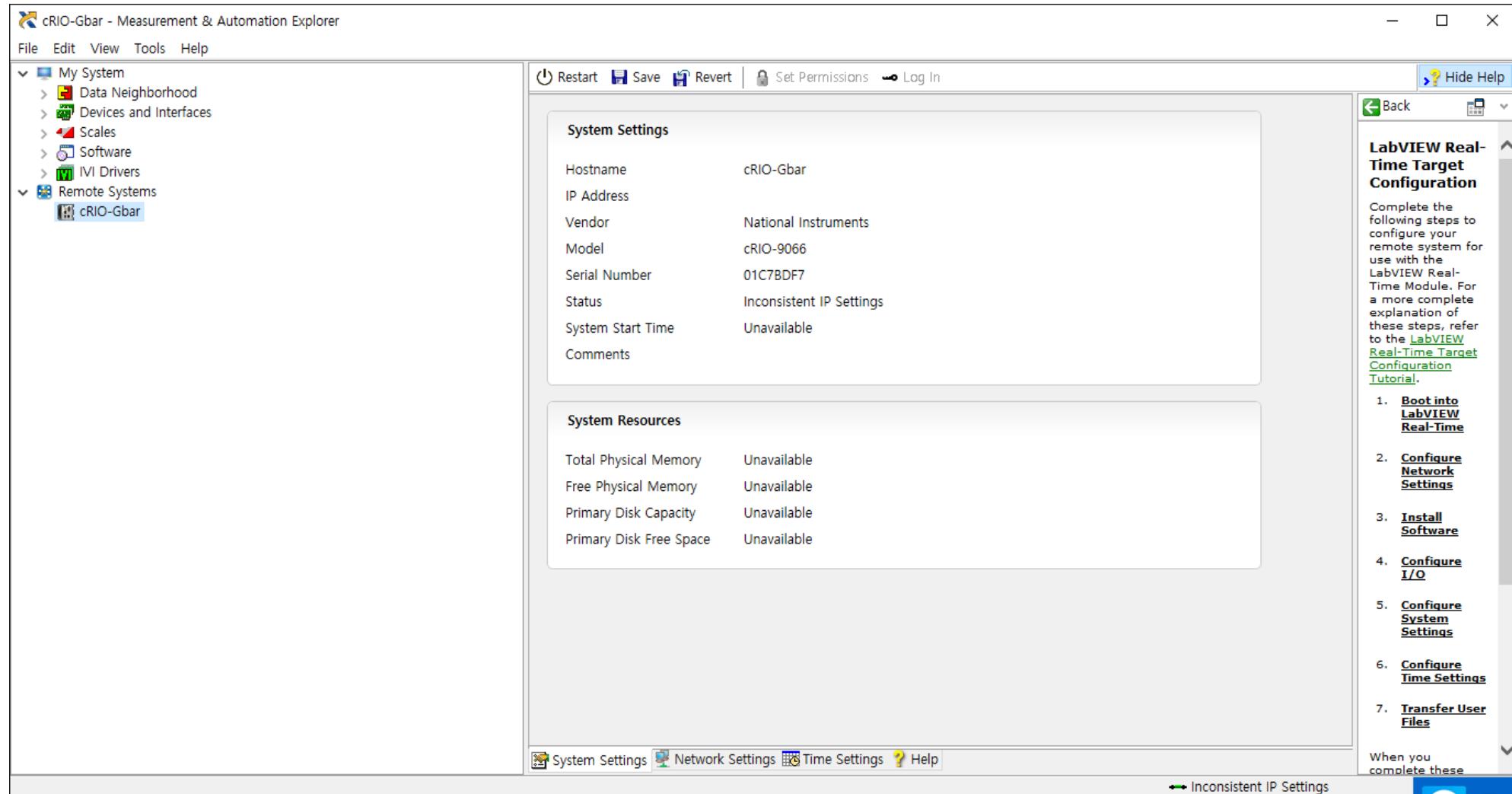


Trap @ Sejong

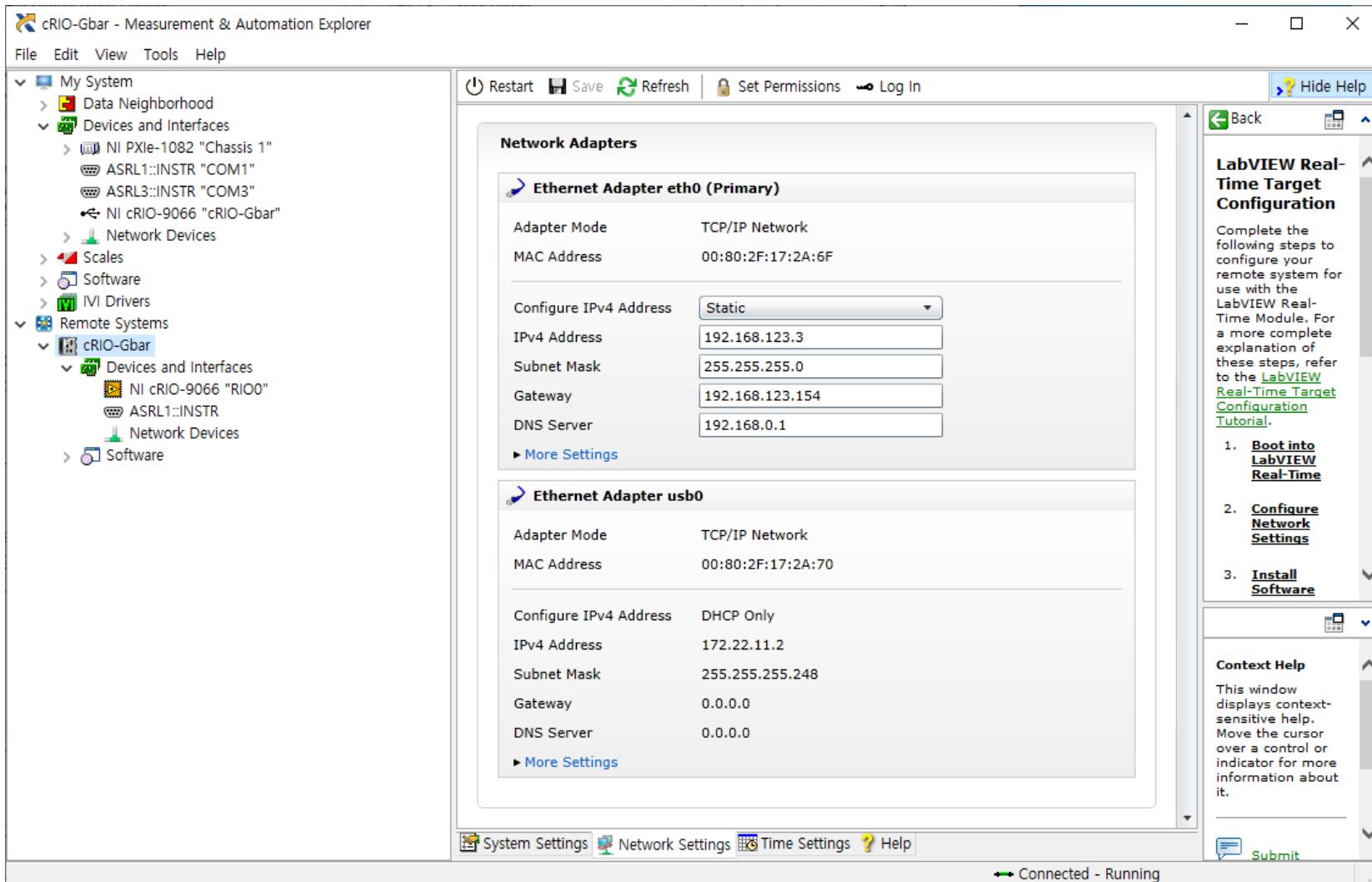
20200311

박관형

PXI-cRIO connection error



PXI-cRIO connection error



PXI-cRIO connection error solved

The image displays two software interfaces side-by-side. On the left is the "Measurement & Automation Explorer" (MAE) showing a tree view of system resources, including a "cRIO-Gbar" entry under "My System". On the right is the "VISA Test Panel" showing a configuration screen for a device at "visa://172.22.11.2/ASRL2::INSTR". The configuration tab is selected, displaying settings like Name (ASRL1::INSTR), Port Binding (/dev/ttyS0), and Port Description (ASRL1 (/dev/ttyS0)). The Input/Output tab shows a basic I/O interface where a write command "#000F\r\n" has been sent (Return Count: 6 bytes) and a read response has been received (Return Count: 51 bytes, starting with ">0.000E+00,0.000E+00,0.000E+00,OPEN\s\s\s\s\s,9.958E+02\r").

etc

- One more AO module 6733 (1+1)
- Remaining parts of Magnet



Todo

- LabVIEW memory usage monitor
- E-trap test
- 전문연구요원 서류