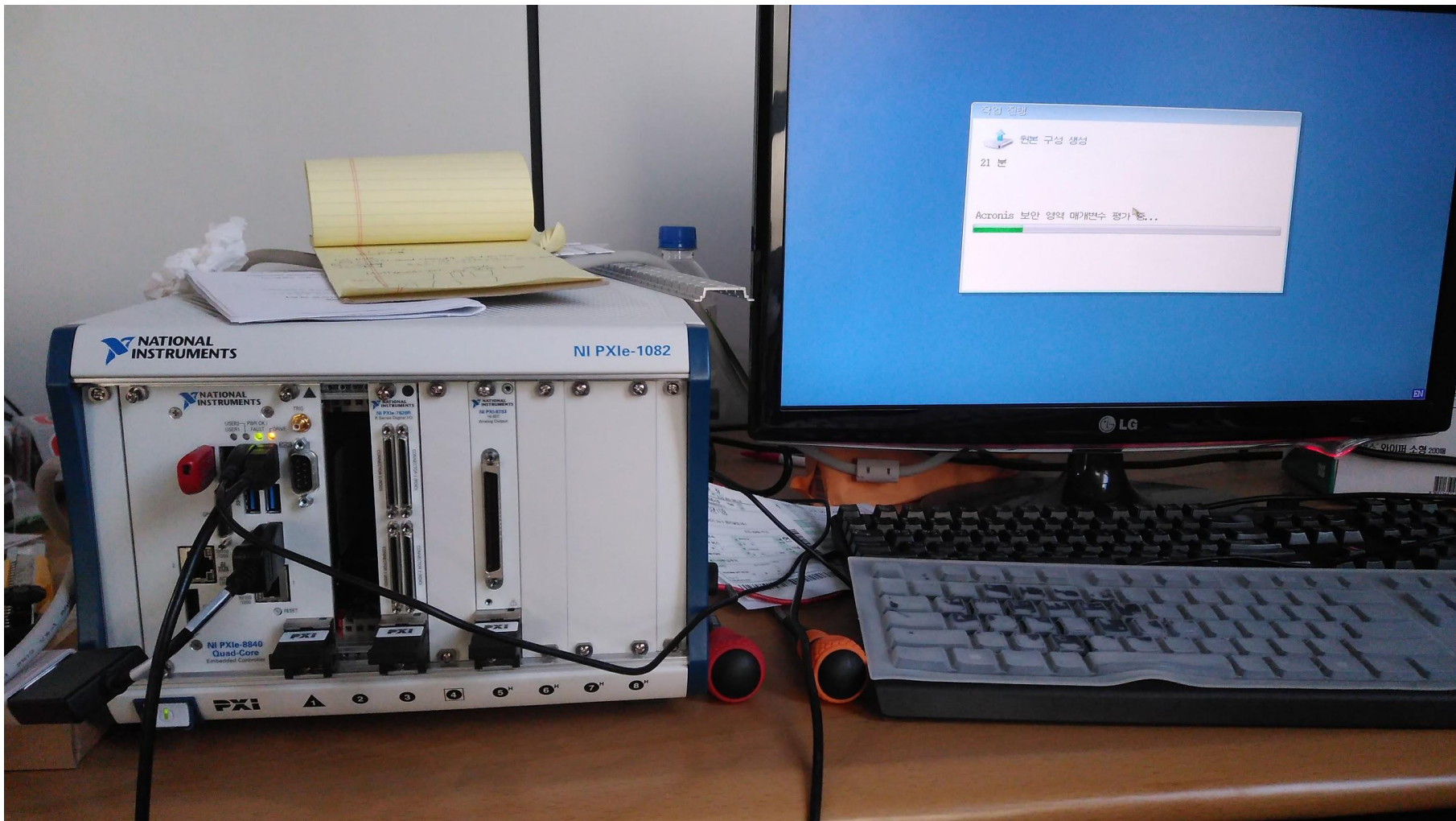
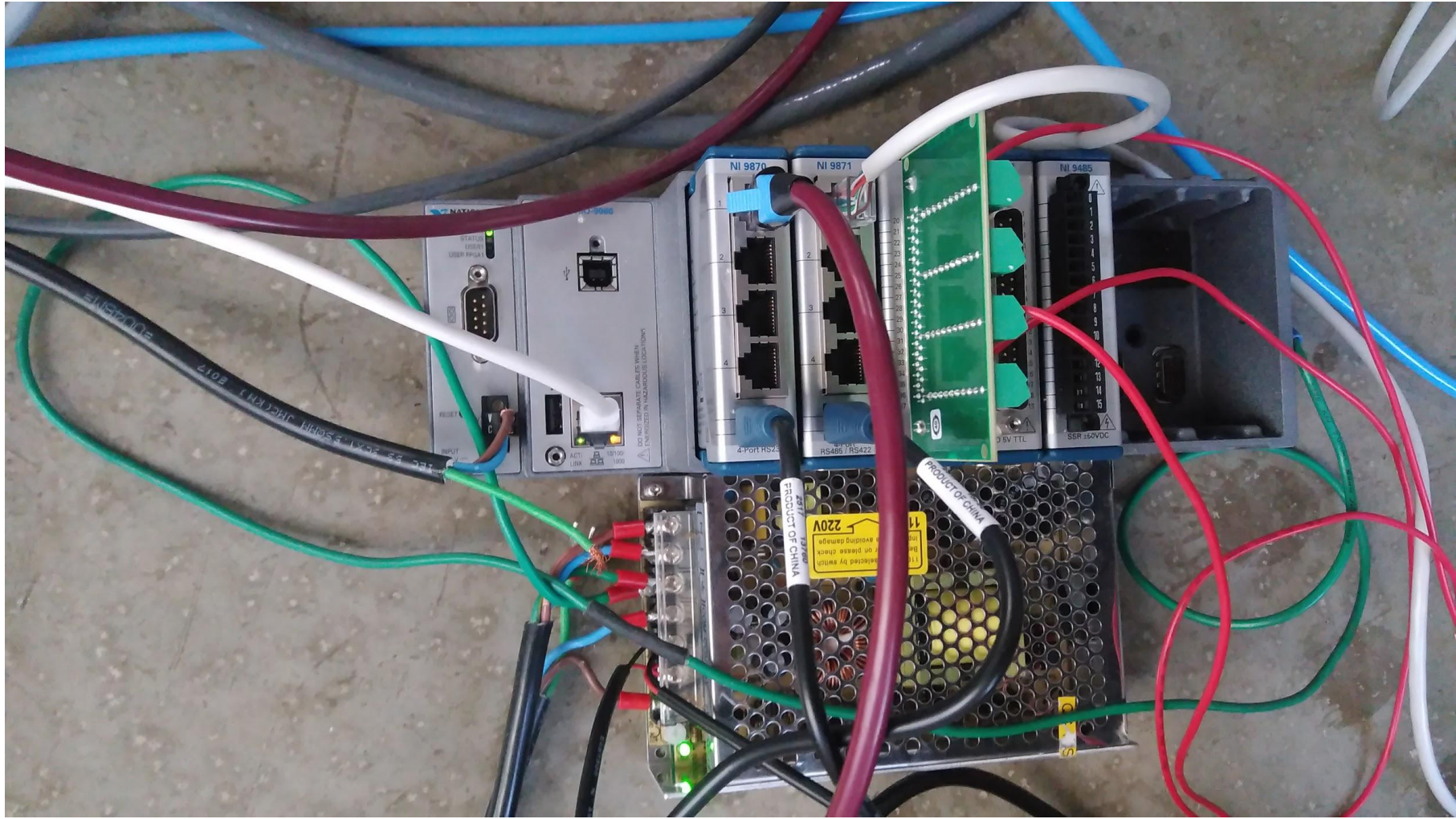
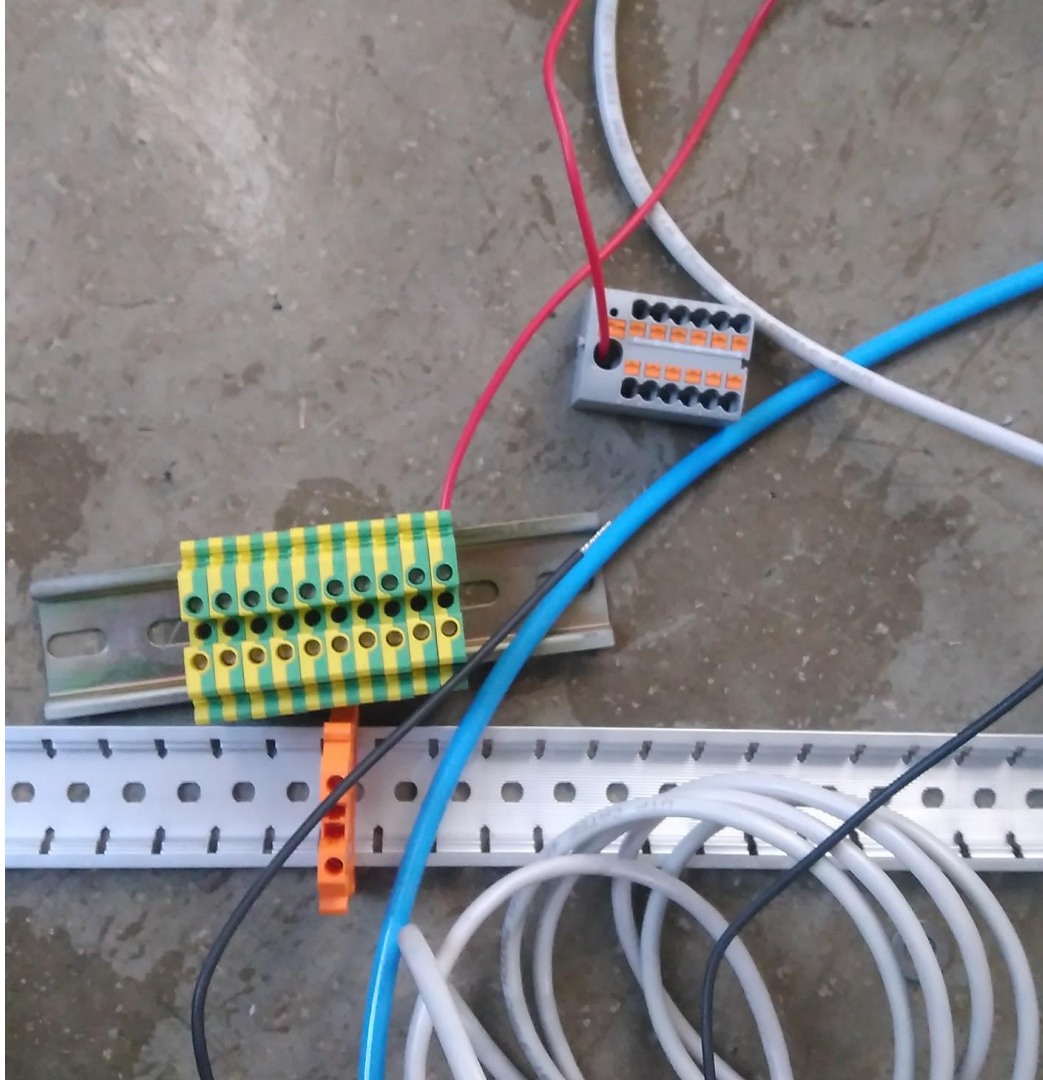


Trap
PXI and etc







- HV module control
 - TTL signal(PXI-7820R)

- FPGA
 - Trigger & Safaty condition